

Destination Part List (SW Control)						
SXX	LOC		U	C	A	
863	K451	V43020			X	X
		MSJ-059-124PC				
864		V450530			X	X
		DT144EXA				
865	4462	V487260			X	X
		A1700815/11				
866		V450530			X	X
		DT144EXA				
867	L452	V424890				
		BLNCHPNC030				
868	C500	8H3747			X	X
		Q1716				
869	C501	Q1716				
		Q1716				
870	R075	WB78420			X	X
	R076	BT1				
871	R072	QD36610			X	X
		1K				
872	R074	QD36710			X	X
		10K				
873	D451	V109380			X	X
		MA90500				

X: NOT USED

NOTICE (mode))
(J)..... JAPAN
(U)..... U.S.A
(C)..... CANADA
(R)..... GENERAL
(Y)..... CHINA
(K)..... KOREA
(A)..... AUSTRALIA
(B)..... BRITISH
(G)..... EUROPE
(L)..... SINGAPORE
(E)..... OTHERS

Pin diagram of the AD633 precision centred multiplier. The chip has 8 pins. Pin 1 is OUT1, Pin 2 is -IN1, Pin 3 is +IN1, Pin 4 is -Vcc, Pin 5 is +IN2, Pin 6 is -IN2, Pin 7 is OUT2, and Pin 8 is +Vcc. The internal circuit shows two op-amp-like blocks with feedback loops.

[illegible]

Figure 1 shows a waveform of the power supply voltage. The graph displays two waveforms, A and B, over time. Waveform A is a square wave that transitions from 5.00 V to 4.00 V at 6.00 V. Waveform B is a square wave that transitions from 5.00 V to 4.00 V at 6.00 V. The x-axis is labeled with time intervals: ch1 5.00 V, 6.00 V, 4.00 V, A, ch1 2.00 V. Below the graph, arrows indicate 'AC POWER ON' at 6.00 V and 'AC POWER OFF' at 4.00 V.

- * 電圧は、内部抵抗10MΩの電圧計で測定したものです。
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- * 本回路図は、標準回路図です。改良のため予告なく変更することがございます。