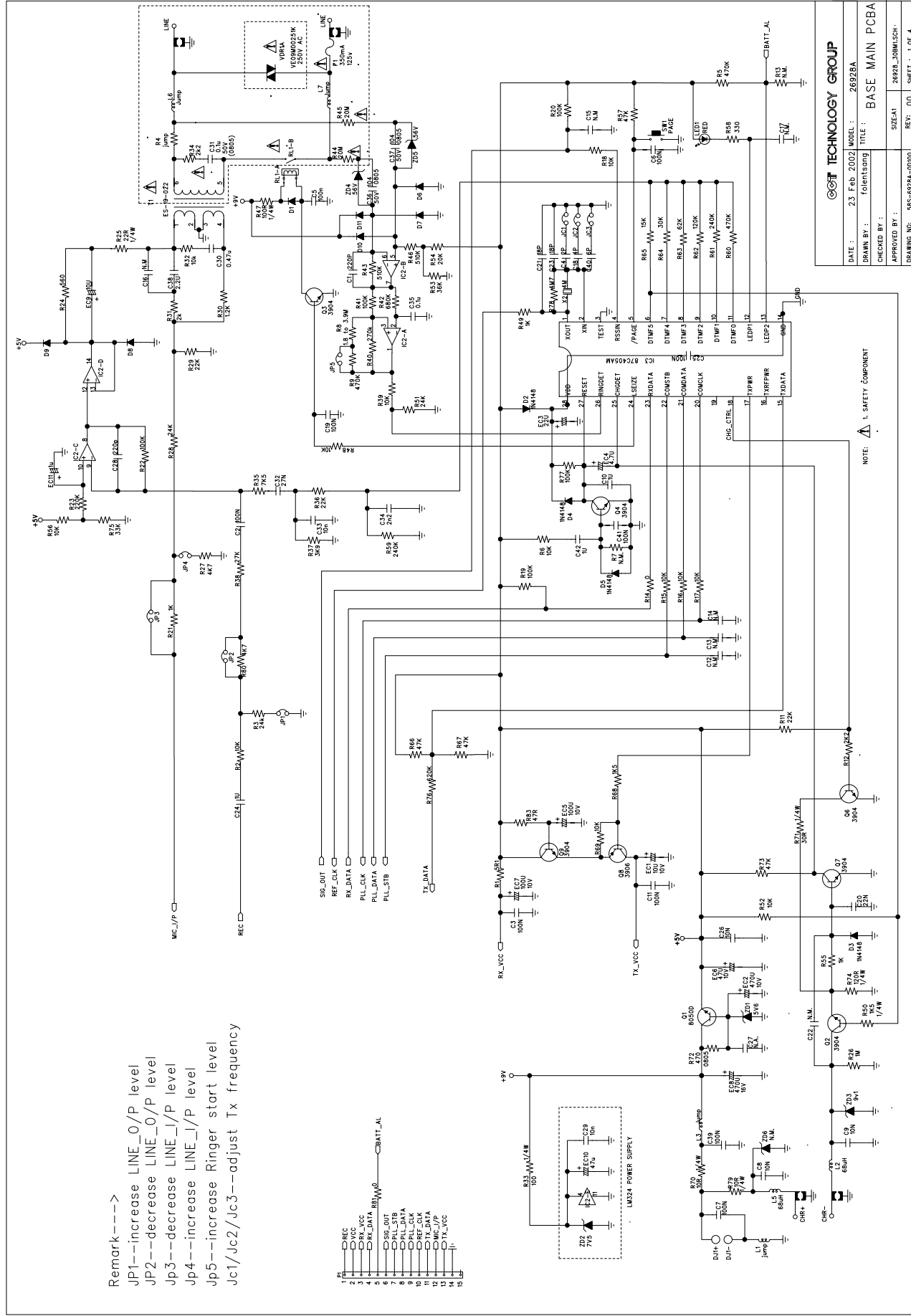


Remark--->
 Jp1---increase LINE_O/P level
 Jp2---decrease LINE_O/P level
 Jp3---decrease LINE_I/P level
 Jp4---increase LINE_I/P level
 Jp5---increase Ringer start level
 Jc1/Jc2/Jc3---adjust Tx frequency

- 1 REC
- 2 VCC
- 3 RX_VCC
- 4 TX_VCC
- 5 PLL_DATA
- 6 PLL_CLK
- 7 PLL_DATA
- 8 PLL_CLK
- 9 PLL_DATA
- 10 PLL_CLK
- 11 PLL_DATA
- 12 PLL_CLK
- 13 PLL_DATA
- 14 TX_VCC
- 15 TX_VCC



DATE : 23 Feb 2002		MODEL : 26928A
DRAWN BY : Tolentsang		TITLE : BASE MAIN PCB
CHECKED BY :		
APPROVED BY :		
DRAWING NO: 585-6928A-0000		SHEET : 1 OF 4

NOTE: 1. SAFETY COMPONENT

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