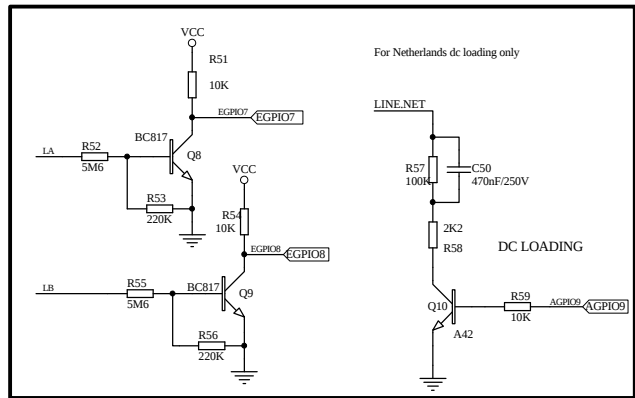
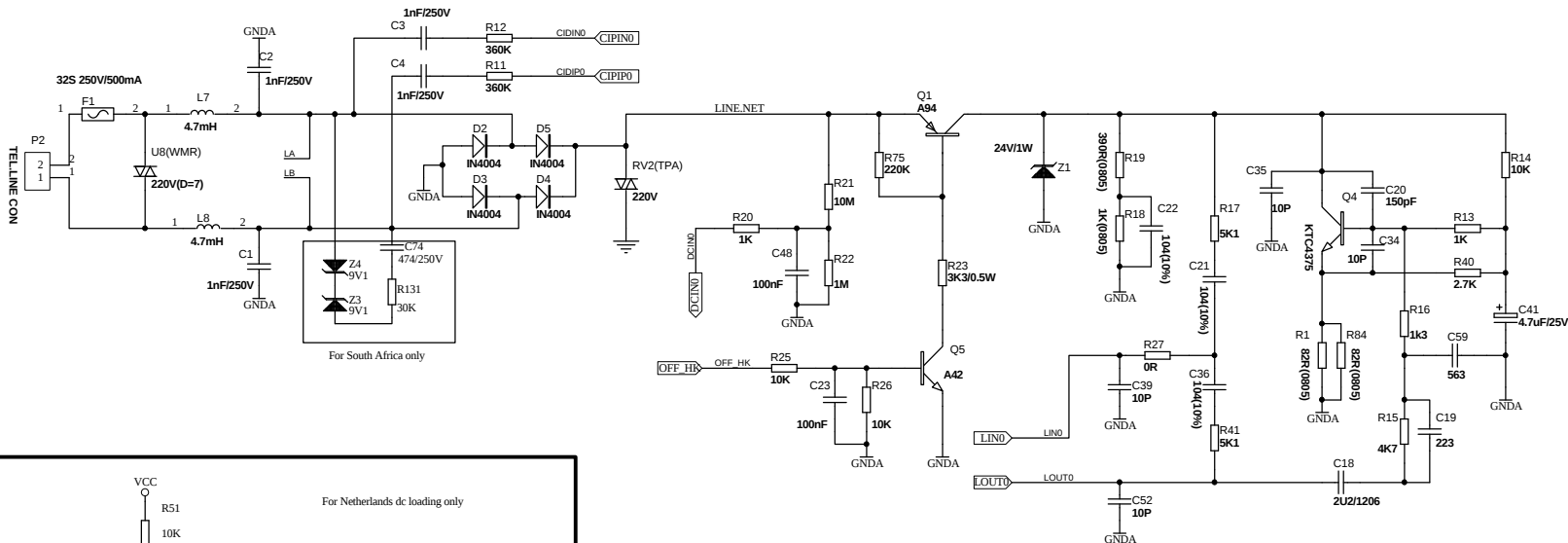
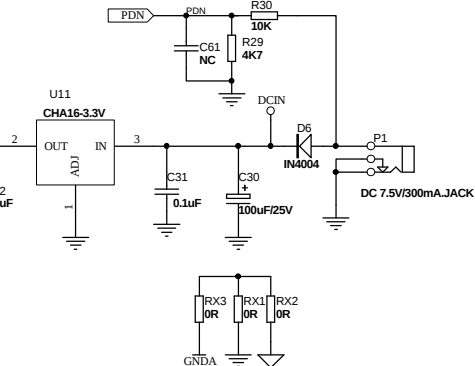
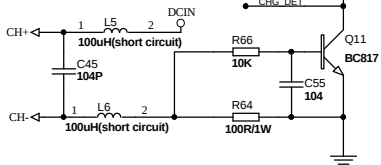
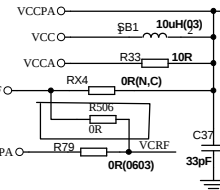
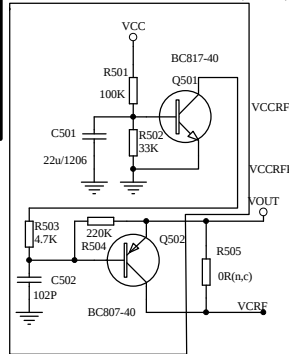




VCCRFPA 电源滤波电容必须更大等于470U(OPTION)



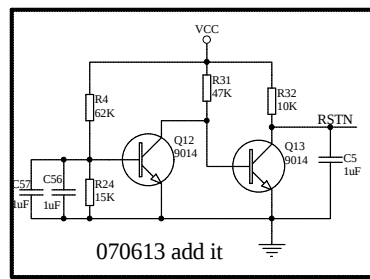
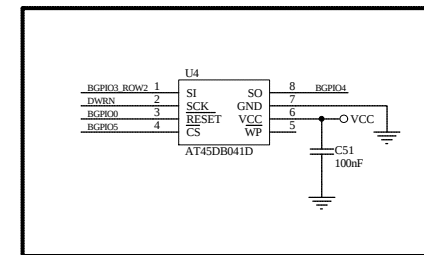
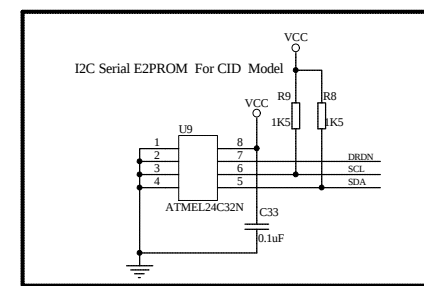
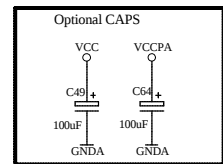
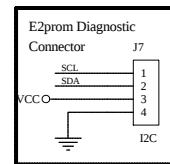
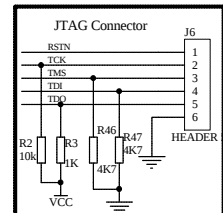
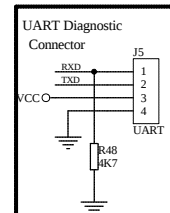
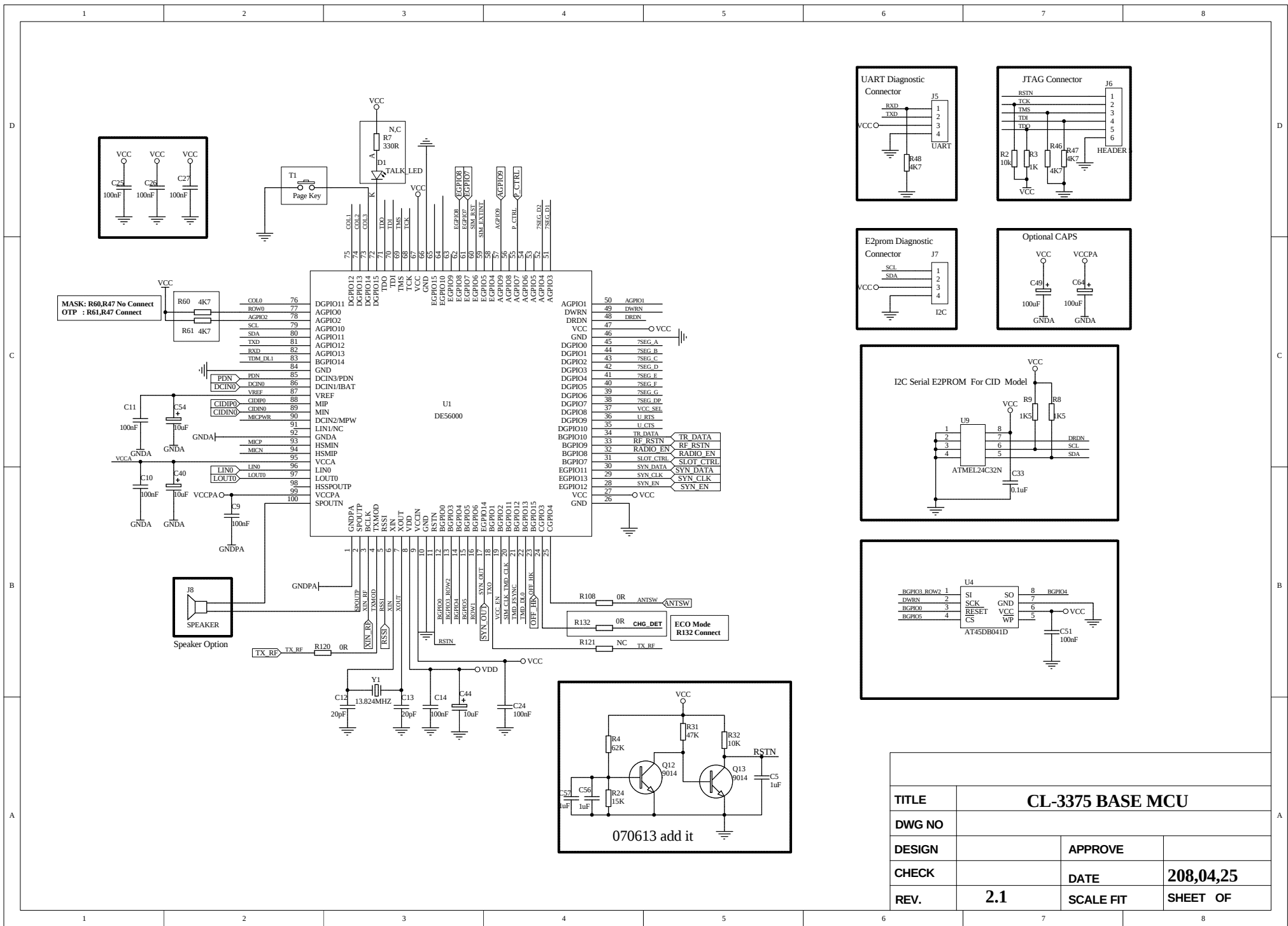
TITLE			
CL3375-LINE(rf19)			
DWG NO			
DESIGN		APPROVE	
CHECK		DATE	
		09/11/30	
REV.		SCALE FIT	
REV 2.1		SHEET 1 OF 3	

CL3307D-BMCU
CL3307D-BMCU.Sch

- OFF_HK
- CIDIP0
- CIDIN0
- PDN
- DCIN0
- LIN0
- LOUT0
- TX_RF
- XIN_RF
- RSI
- ANTSW
- RXGAIN
- P_CTRL
- RXDATA
- RXEN
- TXEN
- RXGAIN
- RXDATA
- ANTSW
- TX_RF

CL3307D-BRF
CL3307D-BRF.Sch

- RSSI
- XIN_RF
- SYN_LE
- SYN_CLK
- SYN_DATA
- SYN_OUT
- RXEN
- TXEN
- RXGAIN
- RXDATA
- ANTSW
- TX_RF



CL-3375 BASE MCU			
TITLE			
DWG NO			
DESIGN	APPROVE		
CHECK	DATE		208,04,25
REV.	2.1	SCALE FIT	SHEET OF

