

# AD1862

## ABSOLUTE MAXIMUM RATINGS\*

$V_L$ to DGND	0 to +13.2 V
$-V_L$ to DGND	$-V_S$ to 0 V
$V_S$ to AGND	0 to +13.2 V
$-V_S$ to AGND	-13.2 to 0 V
AGND to DGND	-0.3 to +0.3 V
Digital Inputs to DGND	-0.3 to $V_L$
Soldering	+300°C, 10 sec
Storage Temperature	-60°C to +100°C

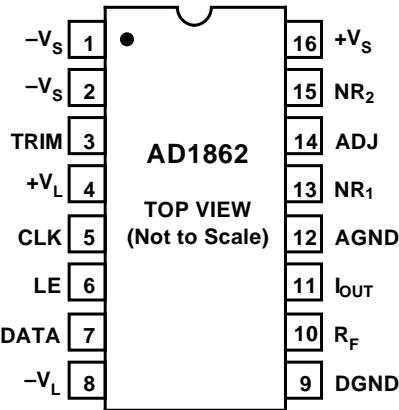
\*Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the AD1862 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



## PIN CONFIGURATION



## PIN DESIGNATIONS

Pin	Function	Description
1	$-V_S$	Bias Capacitor
2	$-V_S$	Analog Negative Supply
3	TRIM	Trim Pot Connection
4	$+V_L$	Positive Logic Supply
5	CLK	External Clock Input
6	LE	Latch Enable Input
7	D	Data Input
8	$-V_L$	Negative Logic Supply
9	DGND	Digital Ground
10	$R_F$	Feedback Resistor
11	$I_{OUT}$	Output Current
12	AGND	Analog Ground
13	$NR_1$	Reference Capacitor
14	ADJ	Midscale Adjust
15	$NR_2$	Bias Capacitor
16	$+V_S$	Positive Analog Supply

## ORDERING GUIDE

Model	Operating Temperature Range	THD+N @ FS	SNR	Package Option*
AD1862N	-25°C to +70°C	-92 dB, 0.0025%	110 dB	N-16
AD1862N-J	-25°C to +70°C	-96 dB, 0.0016%	113 dB	N-16

\*N = Plastic DIP.