



DESCRIPTION

PT6311 is a Vacuum Fluorescent Display (VFD) Controller driven on a 1/8 to 1/16 duty factor. Twelve segment output lines, 8 grid output lines, 8 segment/grid output drive lines, one display memory, control circuit, key scan circuit are all incorporated into a single chip to build a highly reliable peripheral device for a single chip micro computer. Serial data is fed to PT6311 via a three-line serial interface. It is housed in a 52-pin plastic QFP package and is functionally compatible with μ pD16311.

FEATURES

- ☐ CMOS Technology
- ☐ Low Power Consumption
- ☐ Key Scanning (12 x 4 matrix)
- ☐ Multiple Display Modes: (12 segments, 16 digits to 20 segments, 8 digits)
- ☐ 8-Step Dimming Circuitry
- ☐ LED Ports Provided (5 channels, 20 mA max.)
- ☐ 4- Bits General Purpose Input Ports Provided
- ☐ Serial Interface for Clock, Data Input, Data Output, Strobe Pins
- ☐ No External Resistors Needed for Driver Outputs
- ☐ Functional Compatibility with μ pD16311
- ☐ Available in 52-pin QFP Package

APPLICATION

- ☐ Microcomputer Peripheral Device



BLOCK DIAGRAM

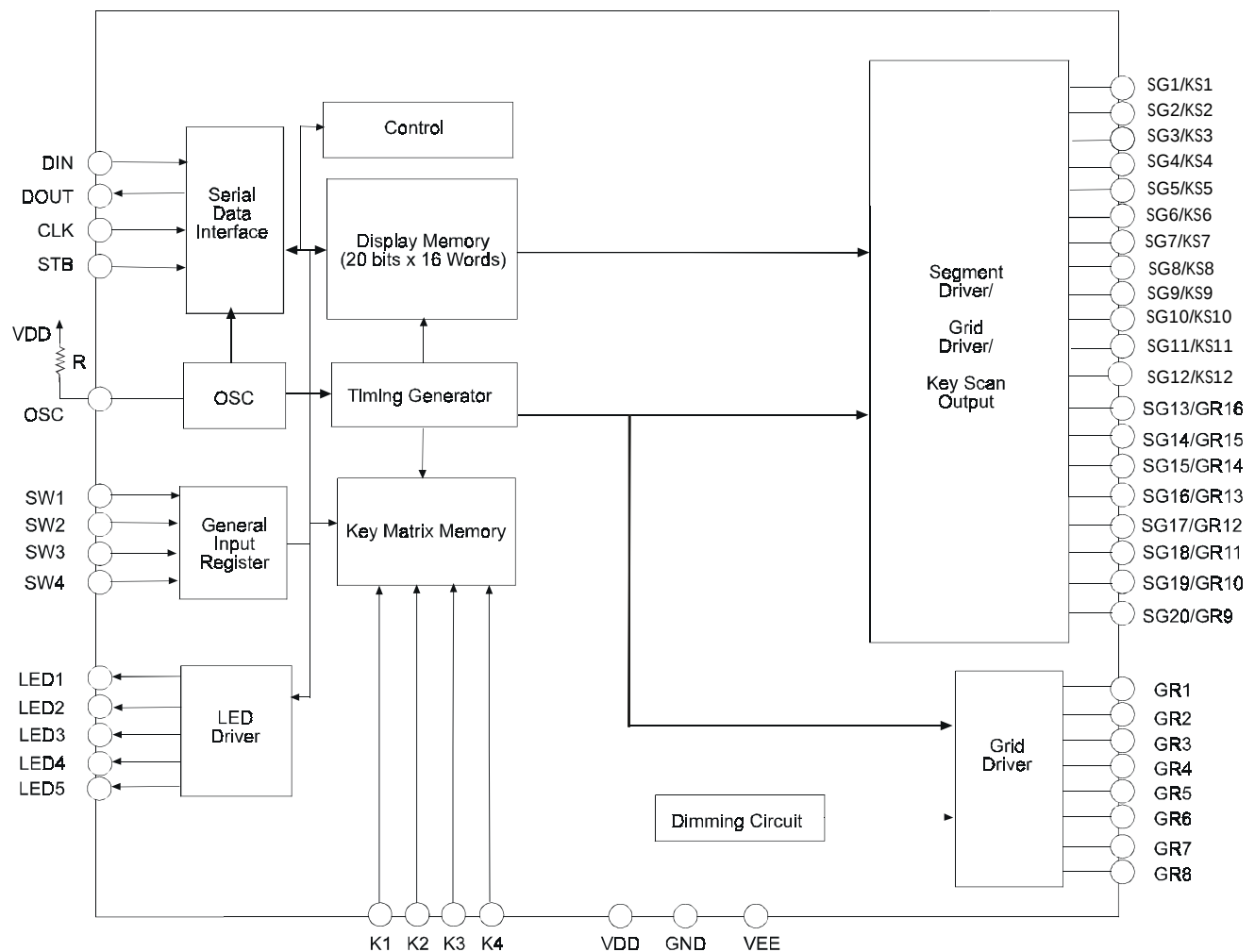


Figure 1: PT6311 Internal Block Diagram



PIN CONFIGURATION

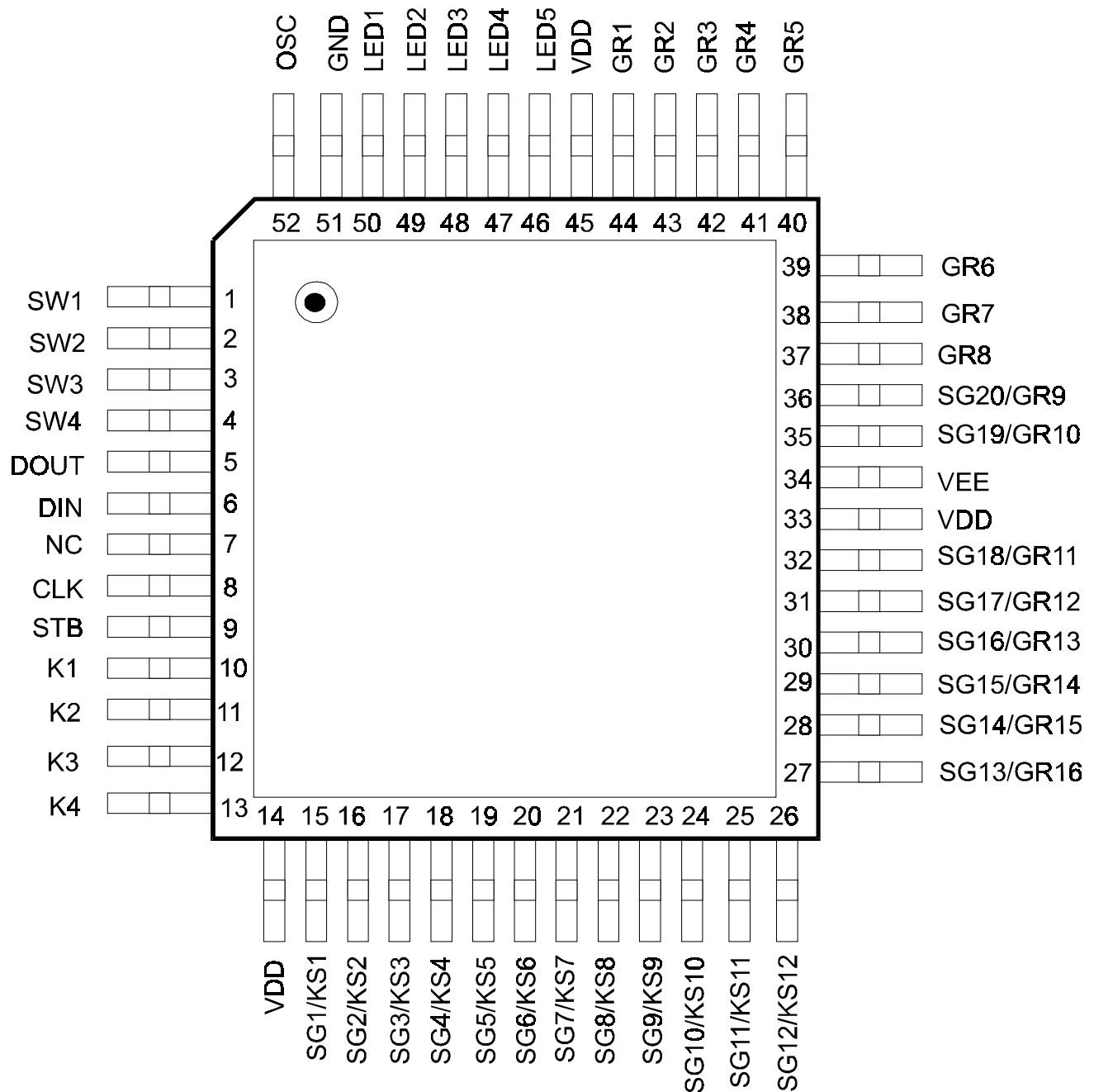


Figure 2: PT6311 Pin Configuration



12-GRID X 16 SEGMENT VFD APPLICATION CIRCUIT

