

MICO

- MSD7816 main feature:
- 1. HD MPEG2 decoding
 - 2. HD H.264 decoding
 - 3. HDMI TX,
 - 4. 1 USB
 - 5. 4xVideo DAC output
 -

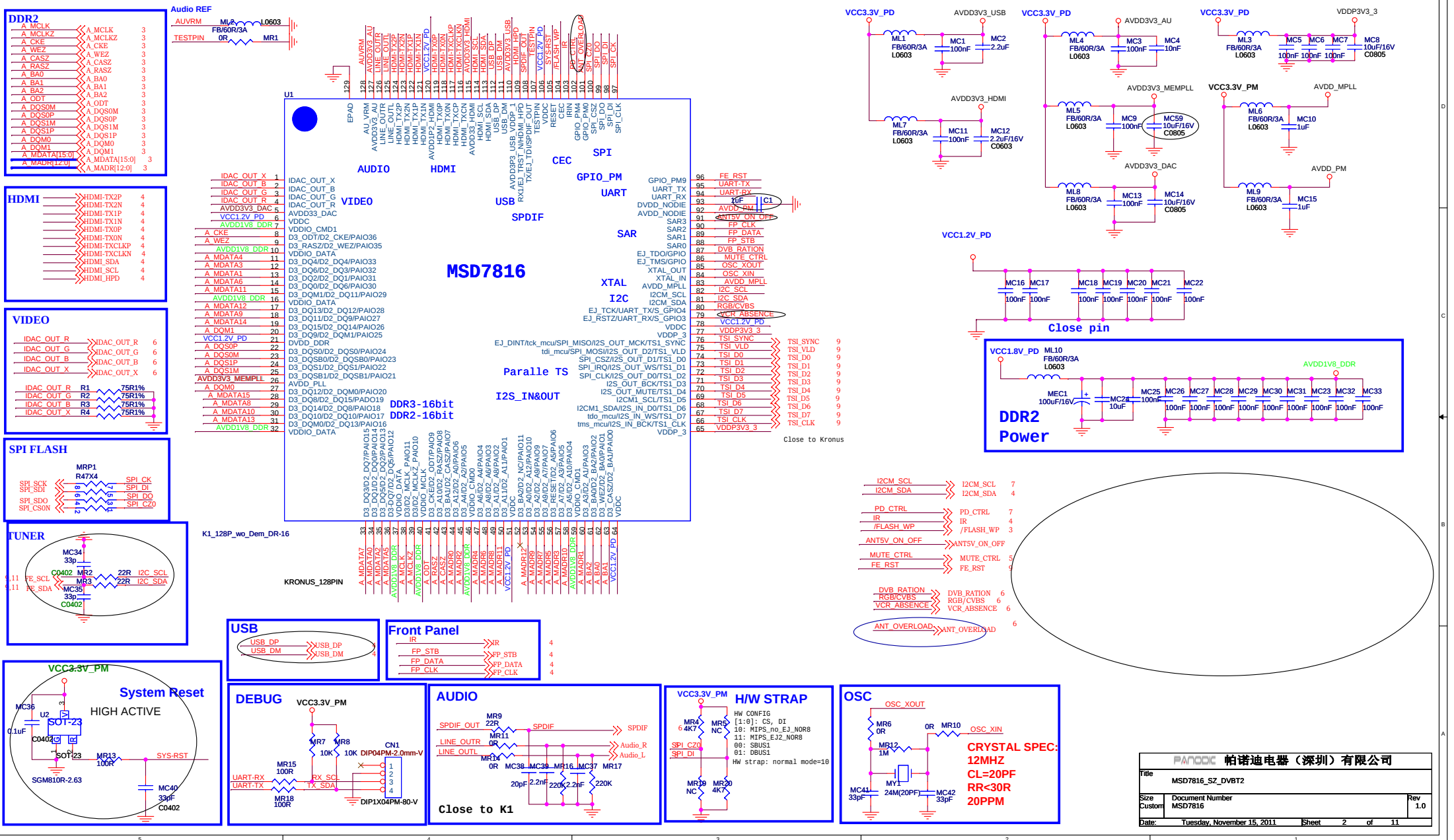
MSD7816_SZ_DVBT2

ChangeList: _____

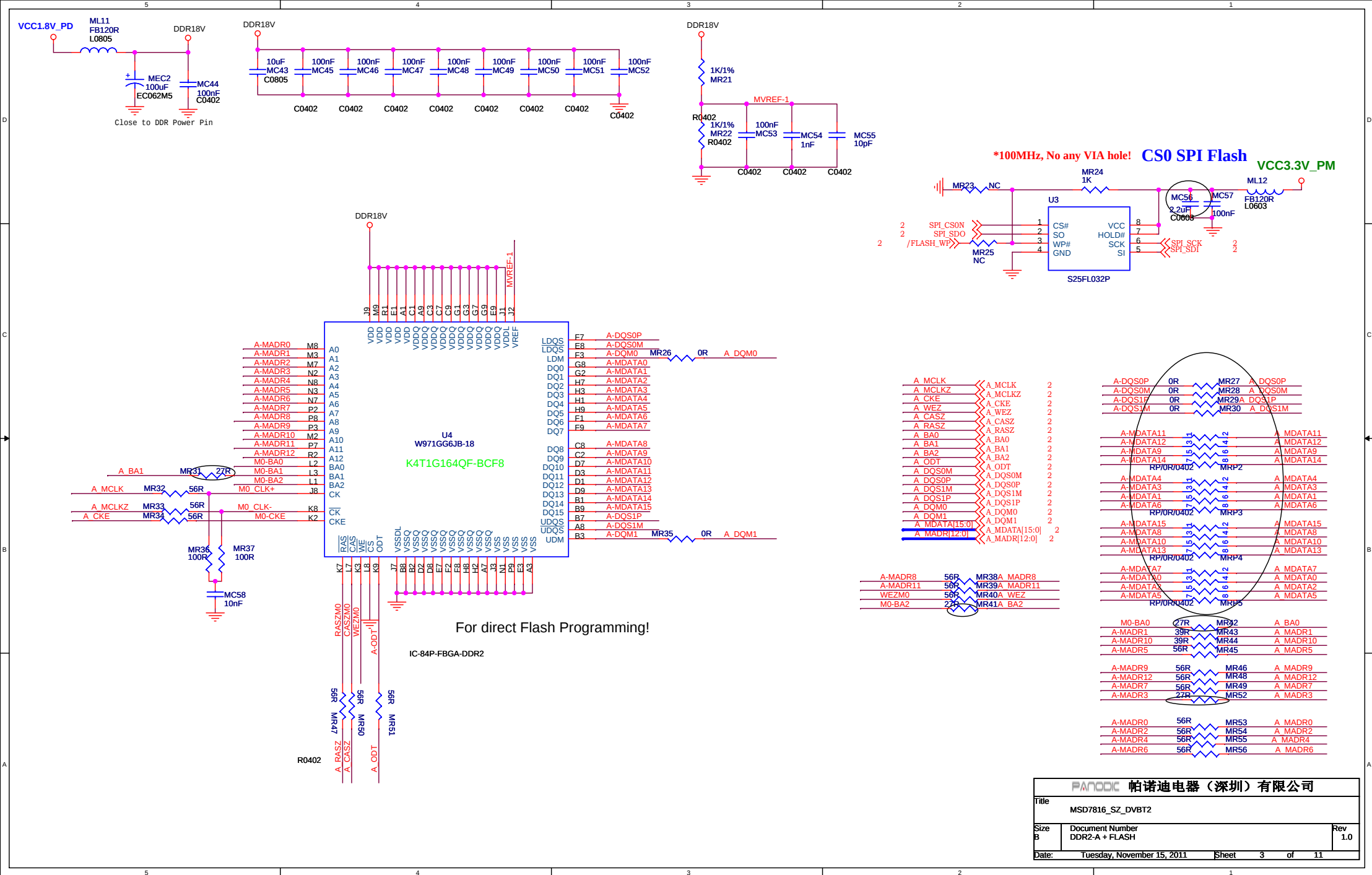
REVISION HISTORY
V1.0 initial release
V1.1 Time:20110504
 Page2 删除 C12、C14、C21、C35、L12、L13for Cost Down,修改C13和C20为1uF, 增加R104,
 Page2 删除 JTAG,
 page3 更改DDR部分的电阻R65、R55、R64、R66、R67、R75以及R60、R49、R51、R52、R53R54、RP2、RP3、RP4、RP5
 page4 C69由10uF(0805)改为2.2uF(0603): 增加R115、R197, 改R116为1K
 page5 改R126为0R, 增加R199(10K)
 page6 修改CVBS为射随线路, 修改Sacrt RGB信号从Buffer后端取, 删除Scart接口的FB和电容

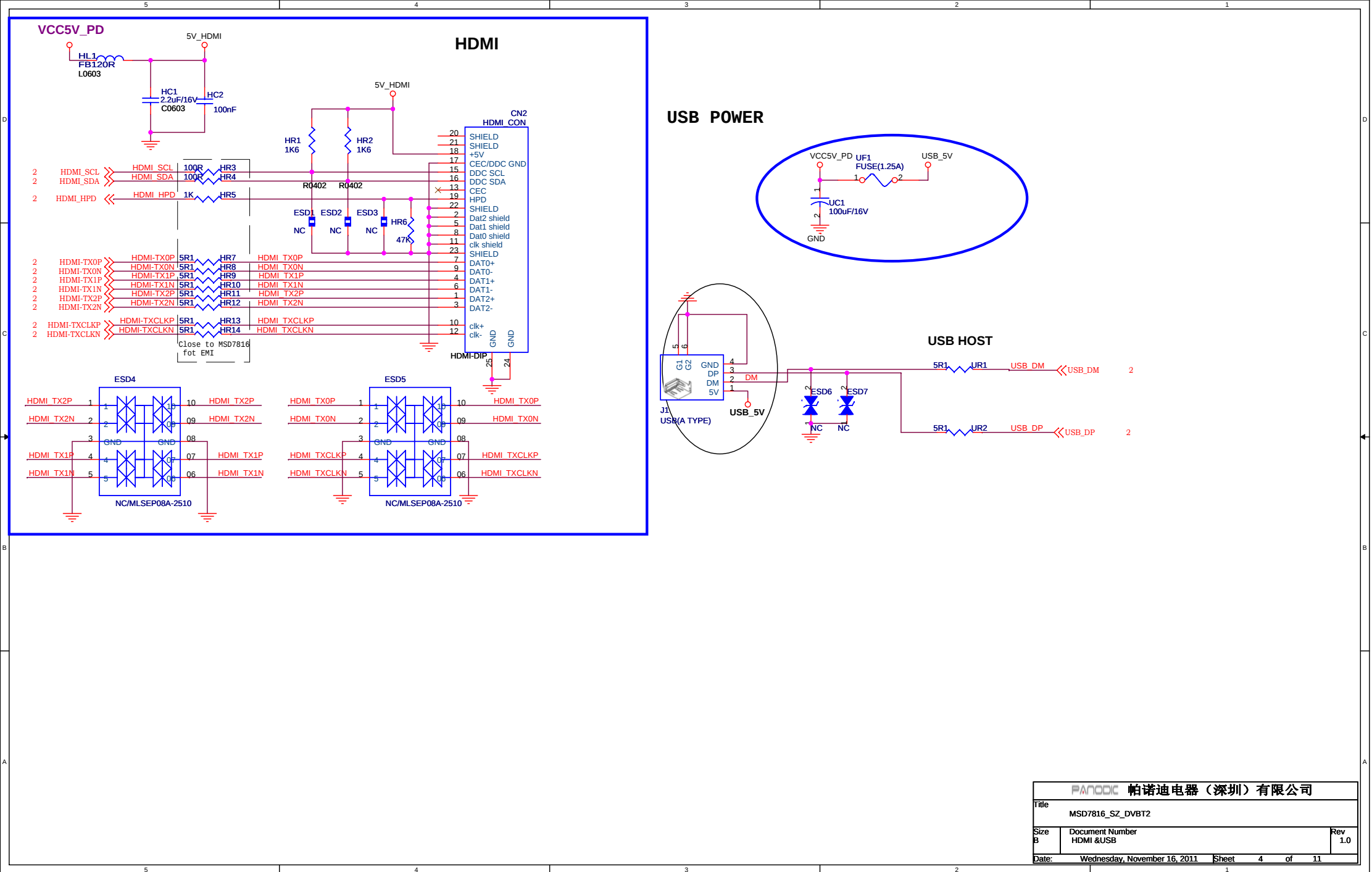
V1.2 2011-05-26
 Page7更改CORE电压为1.3V, R179改为33K/1%, R172改为39K/1%
 Page10 更改TUNER I2C 上拉到VCC3.3V_PD

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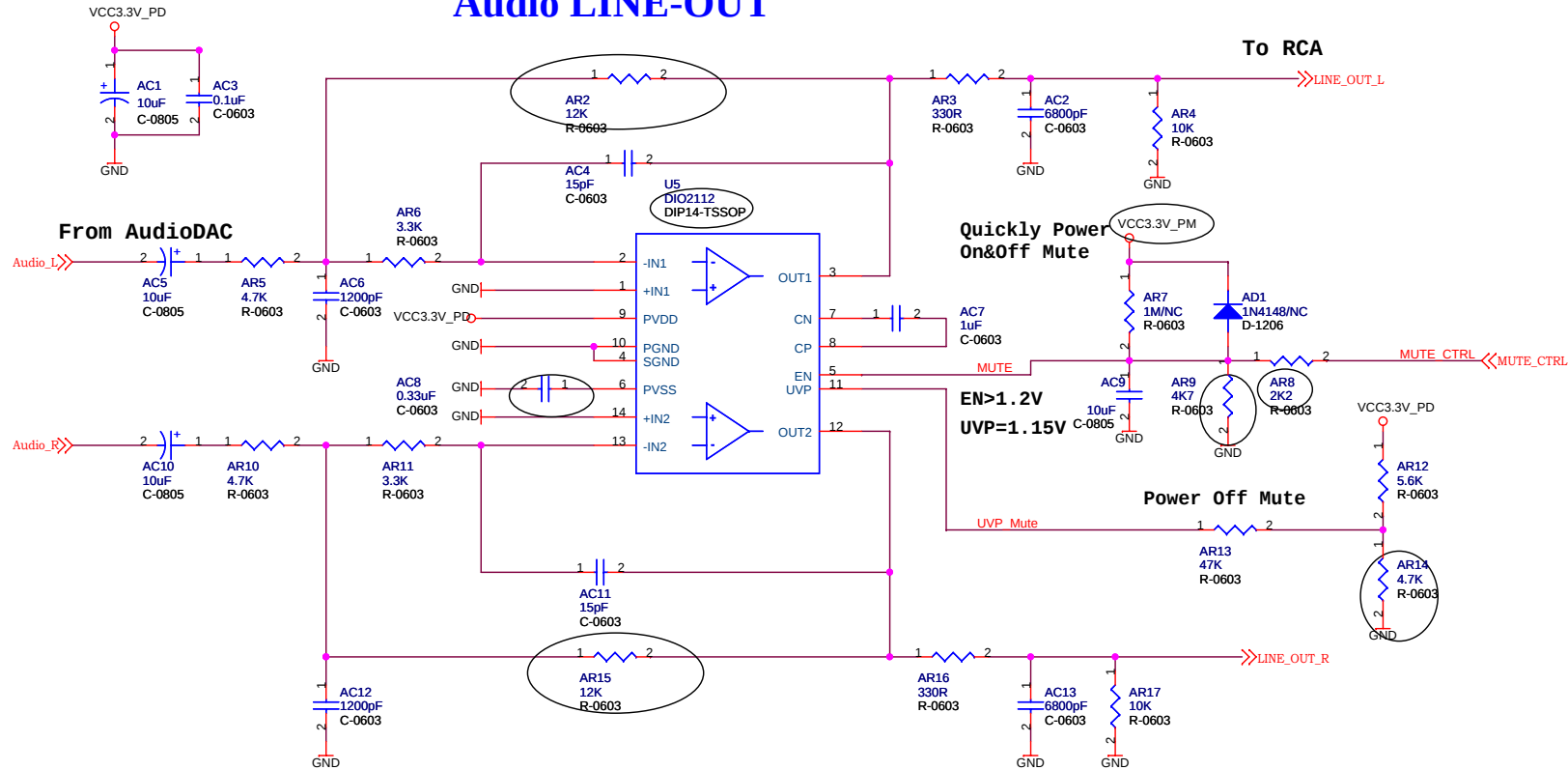
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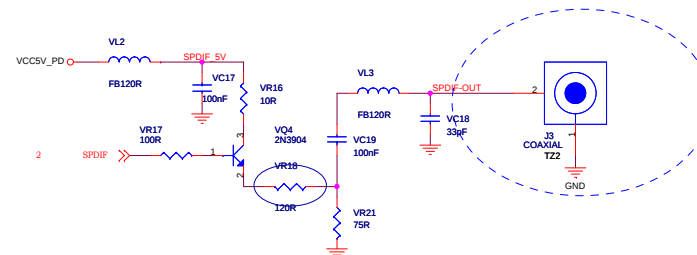
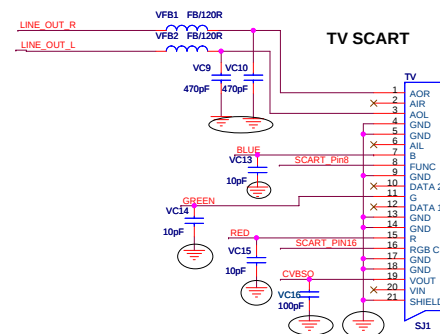
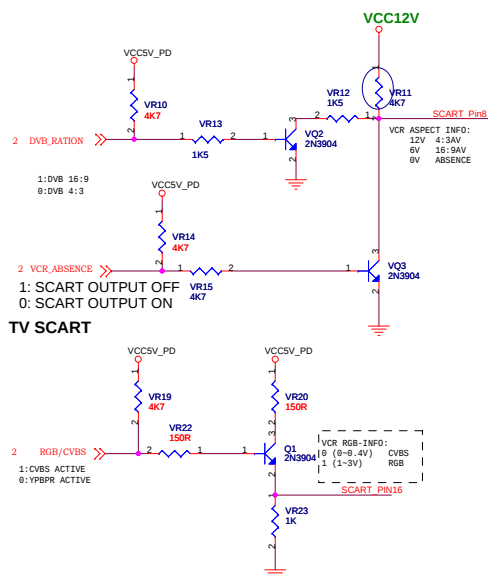
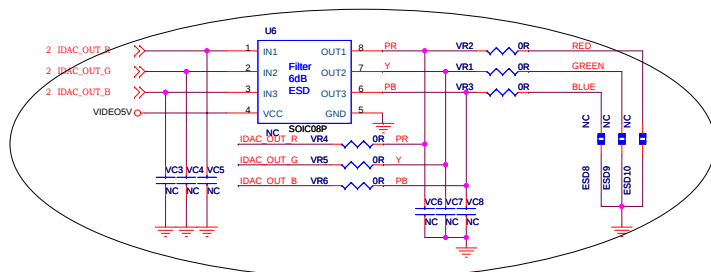
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Title MSD7816_SZ_DVB12		
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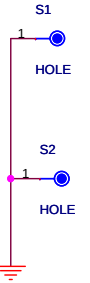
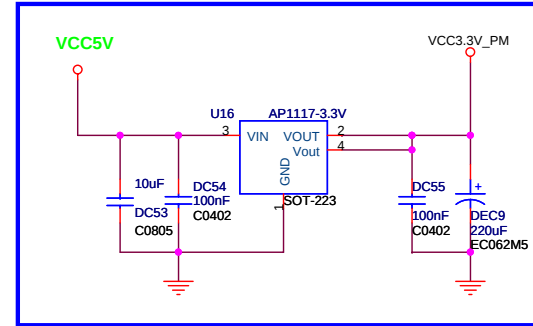
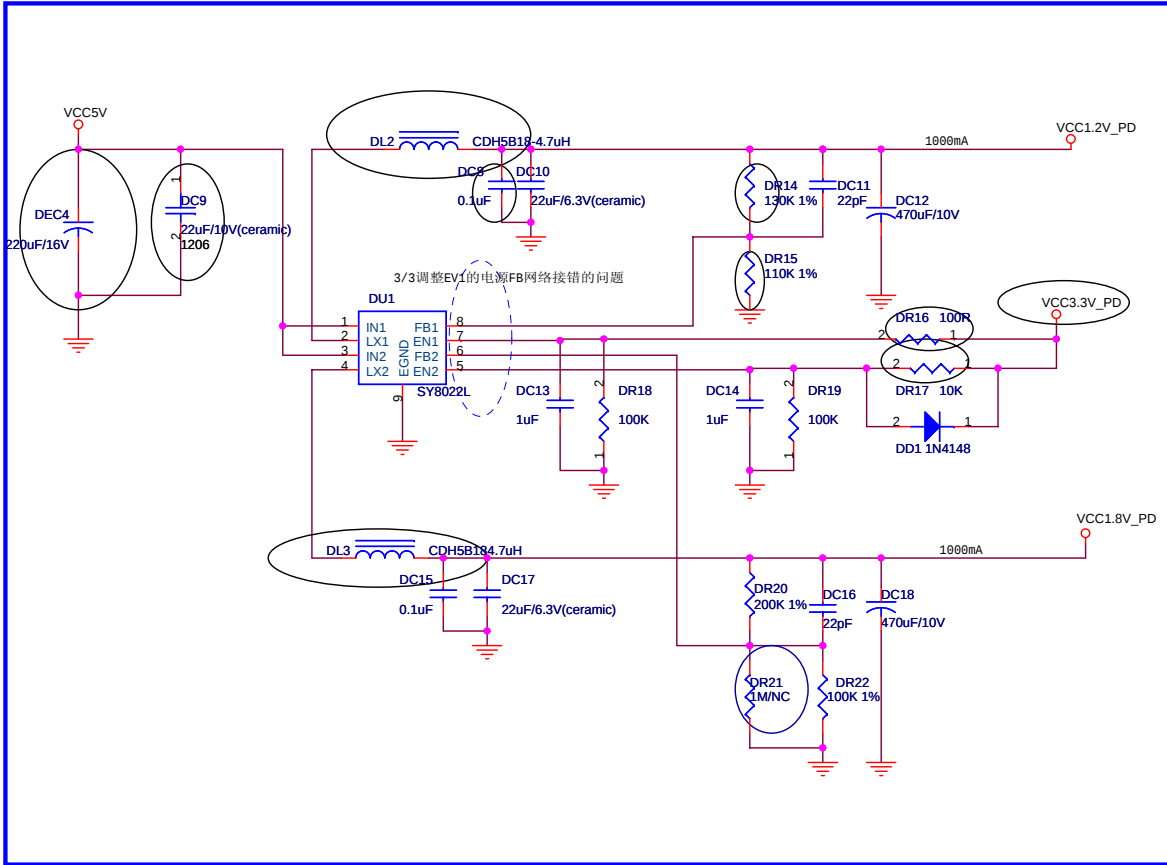
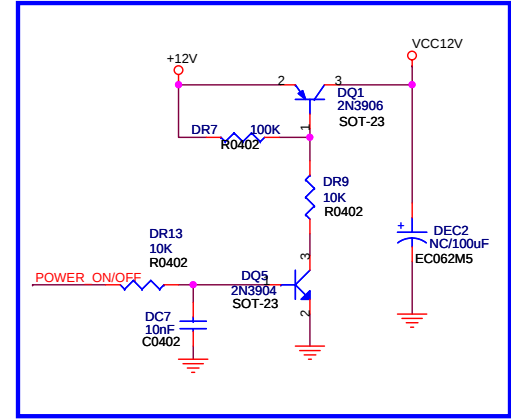
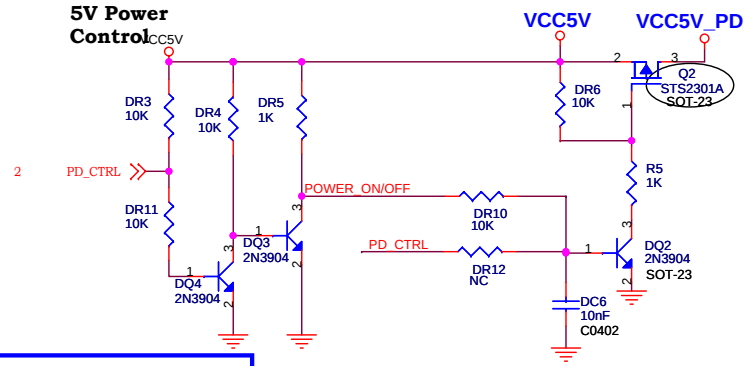
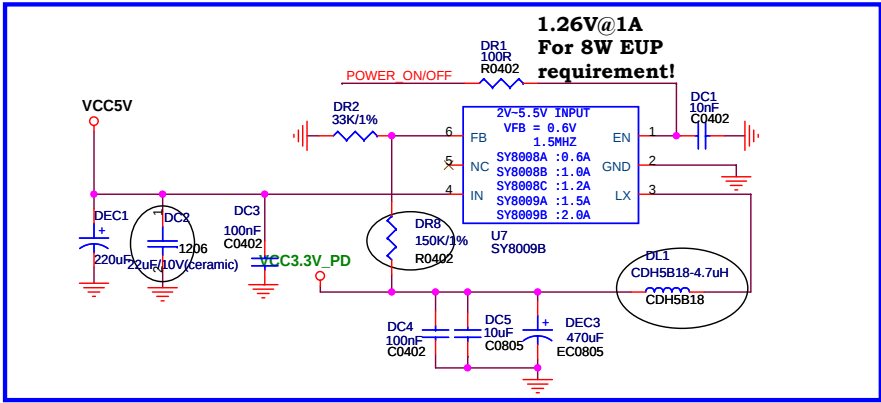
Audio LINE-OUT



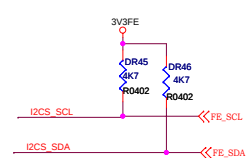
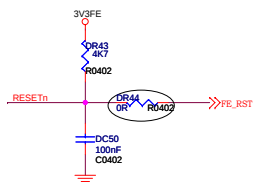
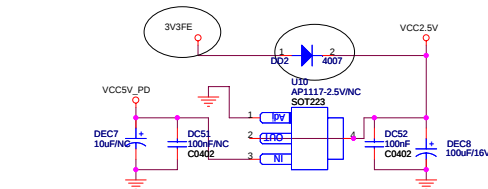
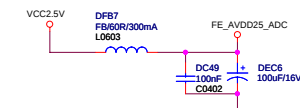
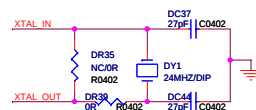
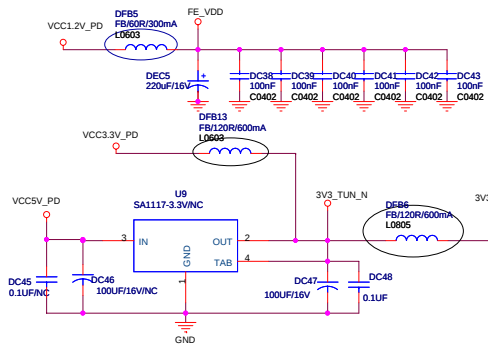
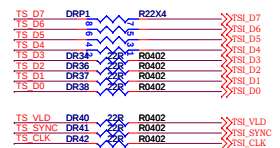
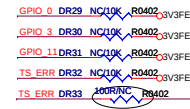
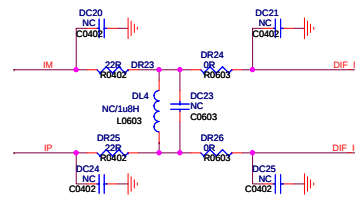
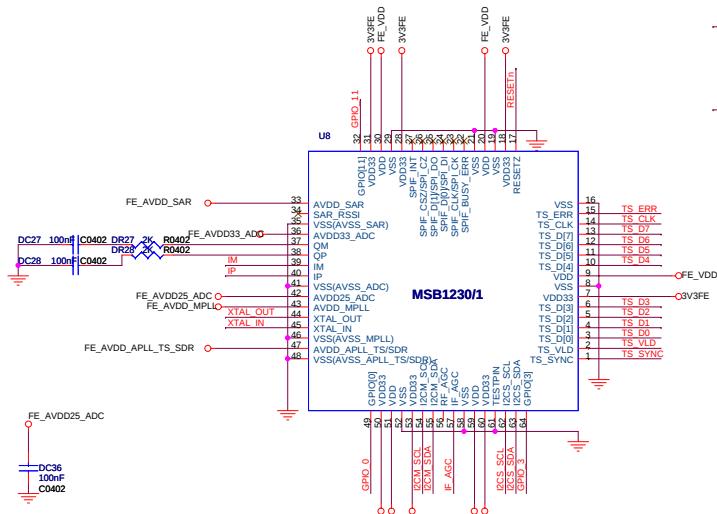
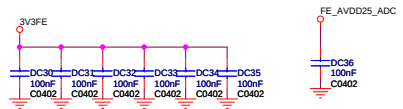
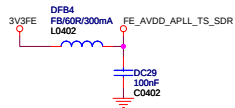
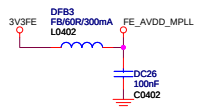
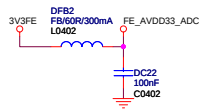
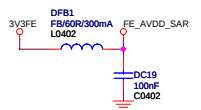
$$V_{out}/V_{in} = -R_{102}/R_{101} = -R_{202}/R_{201}$$

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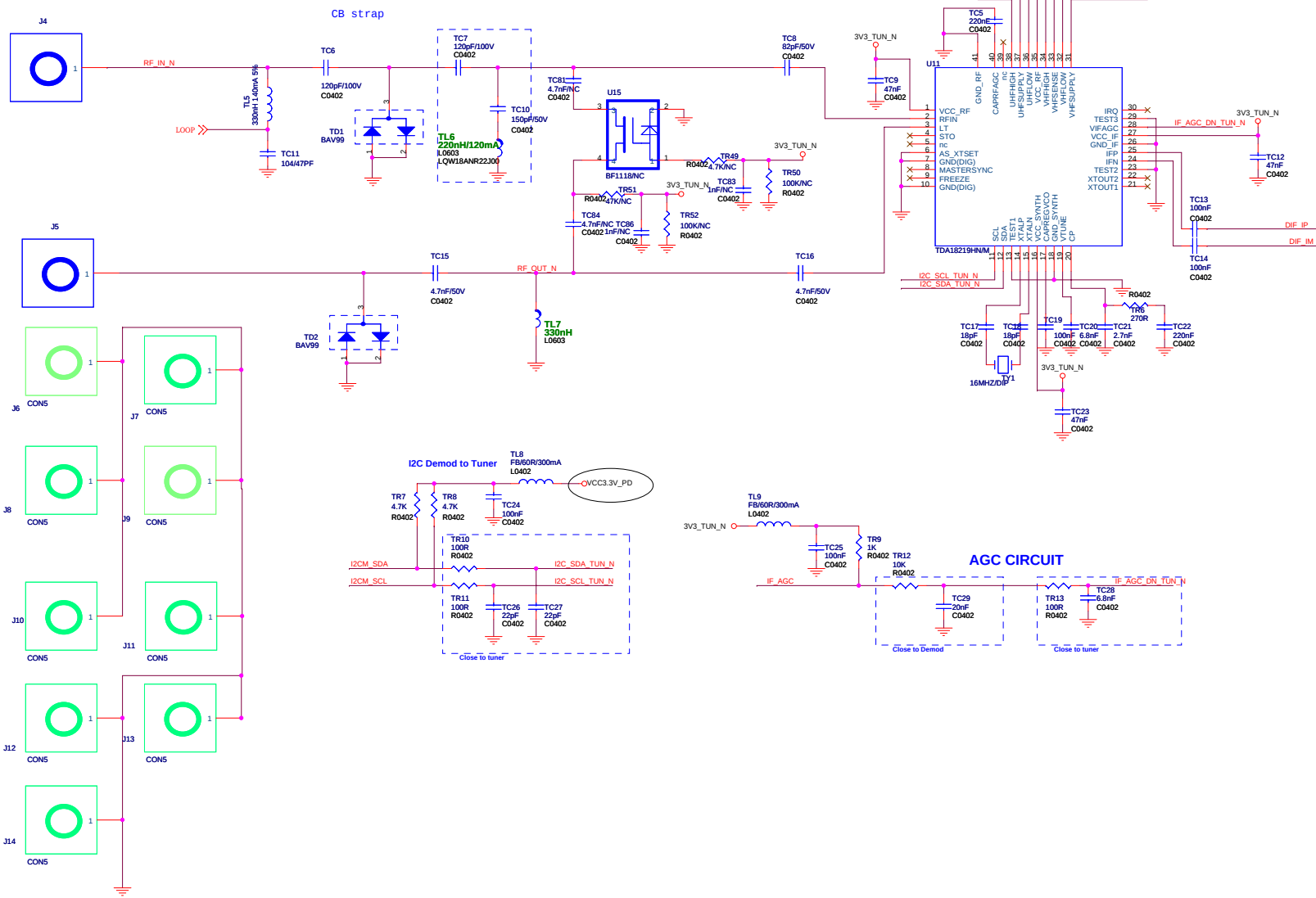
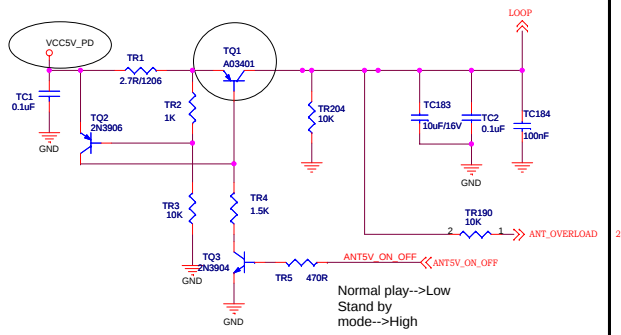




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LOOP



Pin connections for the module:

- DIF_IP (Input)
- DIF_IM (Input)
- IF_AGC (Output)
- I2CM_SCL (Output)
- I2CM_SDA (Output)
- ANT_OVERLOAD (Output)

Front Panel & IR

